



| Form PTO 1449 (Rev. 2-32) | | U.S. Department of Commerce Patent and Trademark Office | | Atty. Docket No. 034299-609 | Serial No. 10/518,649 | | | |
|---|---------------|---|---|--------------------------------|--------------------------|----------|-------------|----|
| Information Disclosure Statement by Applicant (Use several sheets if necessary) | | | Applicant: Dominique Morche Filed: Group: Unassigned 2816 | | | | | |
| U.S. Patent Documents | | | | | | | | |
| Init. | | Document No. | Date | Name | Class | Subclass | Filing Date | |
| D2 | | 6147525 | 11/14/2000 | Kitagawa Nobutaka et al. | / | / | / | |
| D2 | | 6154073 | 11/28/2000 | Choi Jae Myoung | / | / | / | |
| D2 | | 5889436 | 3/30/1999 | Yeung et al. | / | / | / | |
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| Foreign Documents | | | | | | | | |
| | | | | | | | Translation | |
| Init. | | Document No. | Date | Country | Class | Subclass | Yes | No |
| D2 | | 01 45263 A1 | 6/21/2001 | WO | / | / | / | / |
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| Other Documents (Including Author, Title, Date, Pertinent Pages, etc.) | | | | | | | | |
| D2 | | Weinlader, et al. "Ta 10.3 An Eight Channel 36GSample/S CMDS Timing Analyzer", ISSCC 2000/SESSION10/CLOCK GENERATION AND DISTRIBUTION/PAPER TA 10.3, 2000 IEEE International Solid-State Circuits Conference, 0-7803-5853-8/00/\$10.00 © 2000 IEEE, pg. 170-171 | | | | | | |
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| Examiner | <i>Morche</i> | | | Date Considered 12/8/05 | | | | |
| Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant. | | | | | | | | |